TOSOTF 9062600

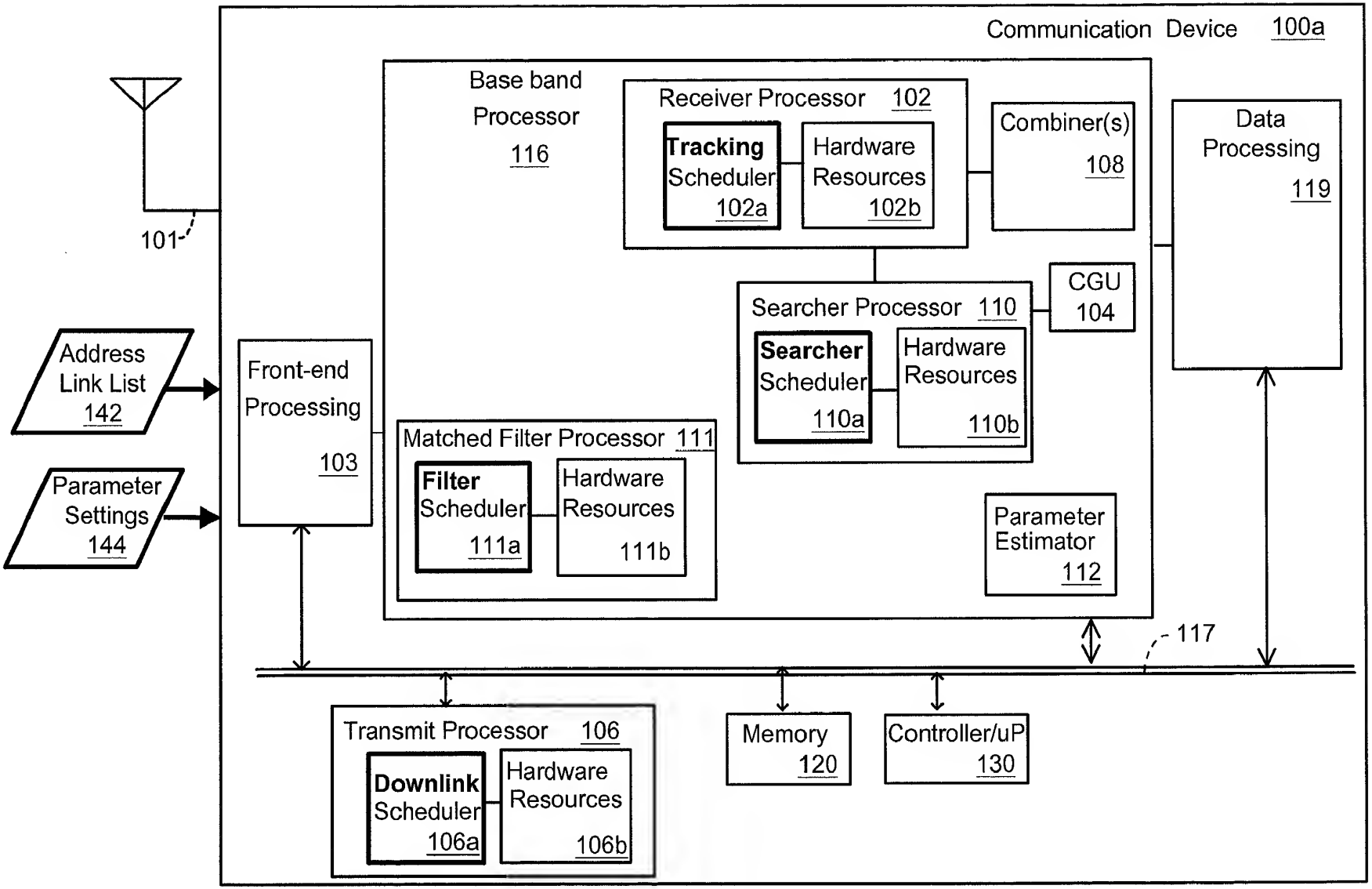


FIGURE 1A

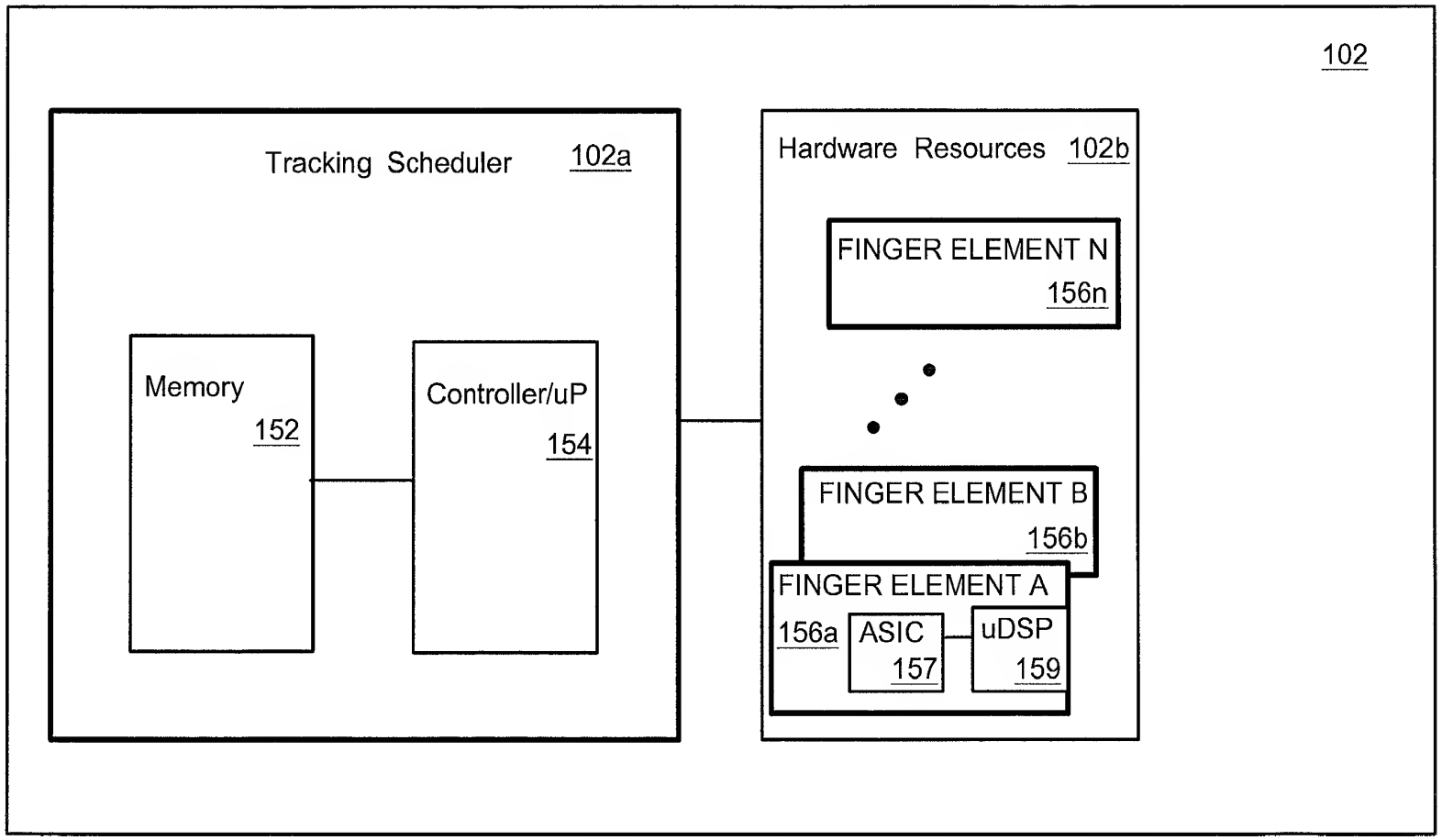

102
Tracking Scheduler 102a
Memory 152
Controller/uP 154
Hardware Resources 102b
FINGER ELEMENT N 156n
FINGER ELEMENT B 156b
FINGER ELEMENT A 156a
ASIC 157
uDSP 159

FIGURE 1B

TOSOFT 9062660

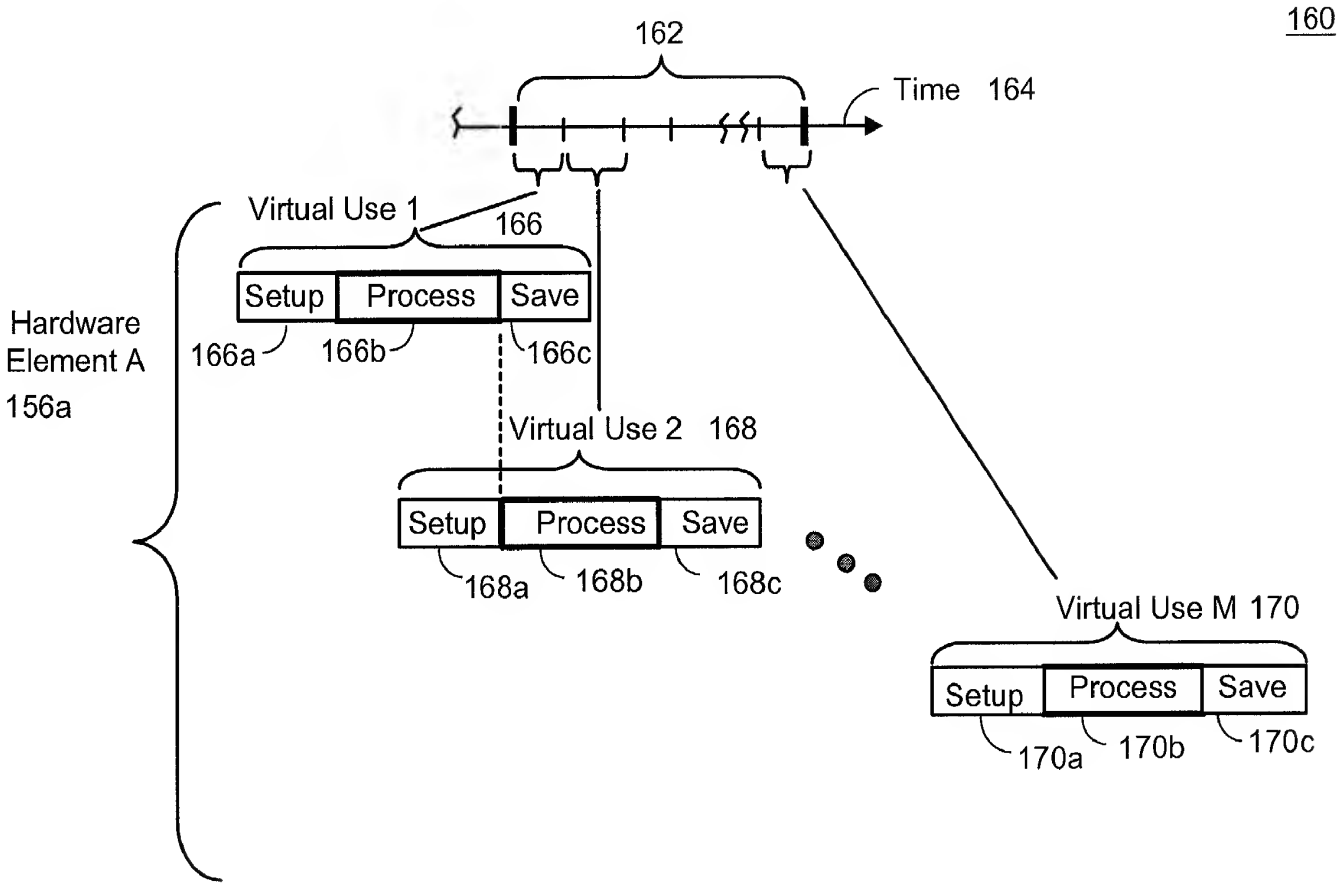


FIGURE 1C

TOSOFT 9062060

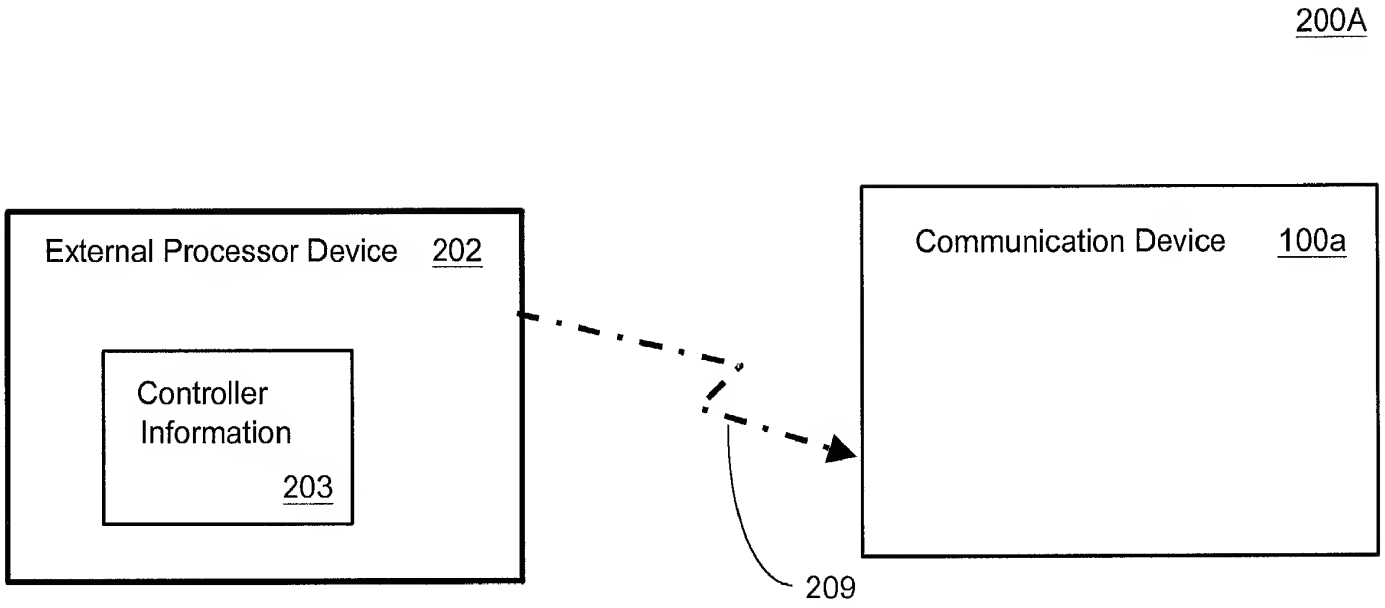


FIGURE 2A

Robert Sobczak

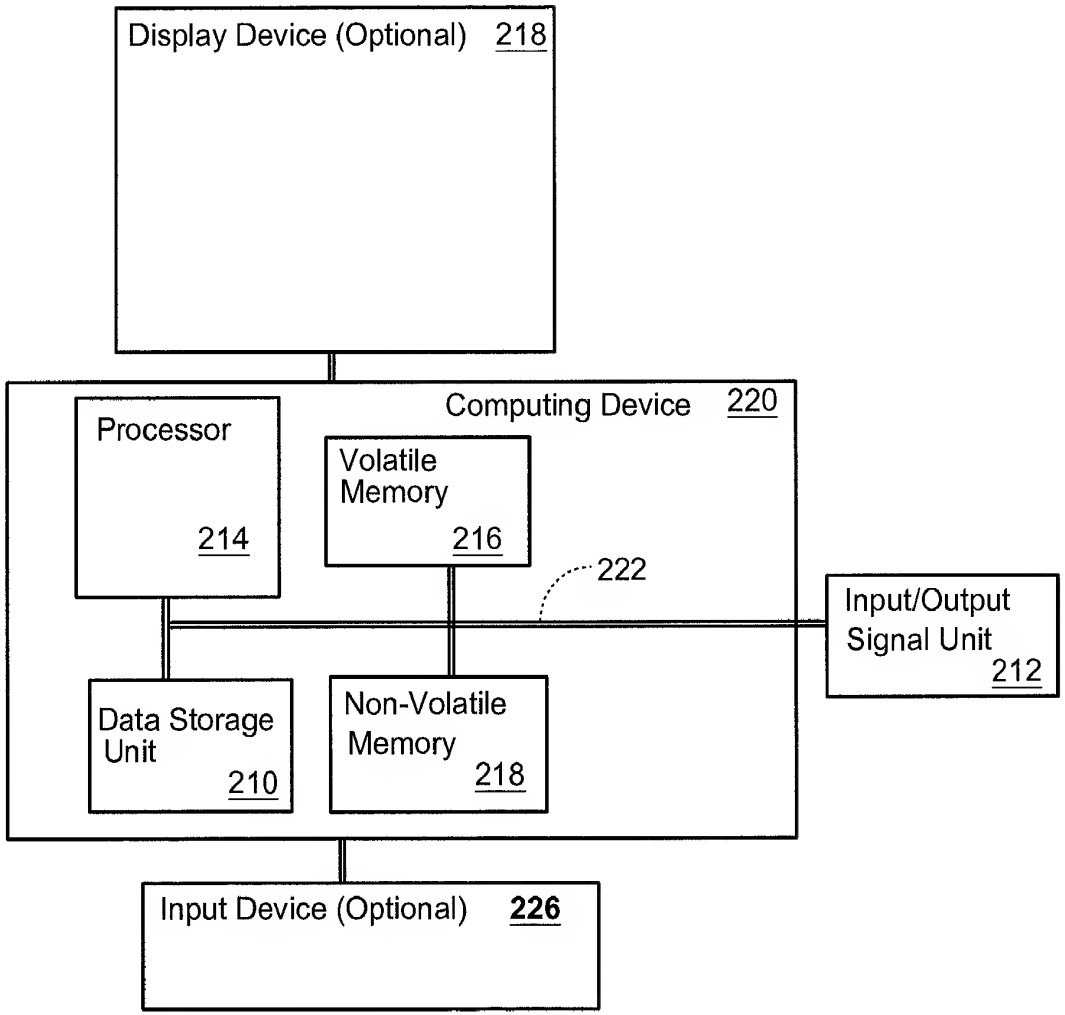


FIGURE 2B

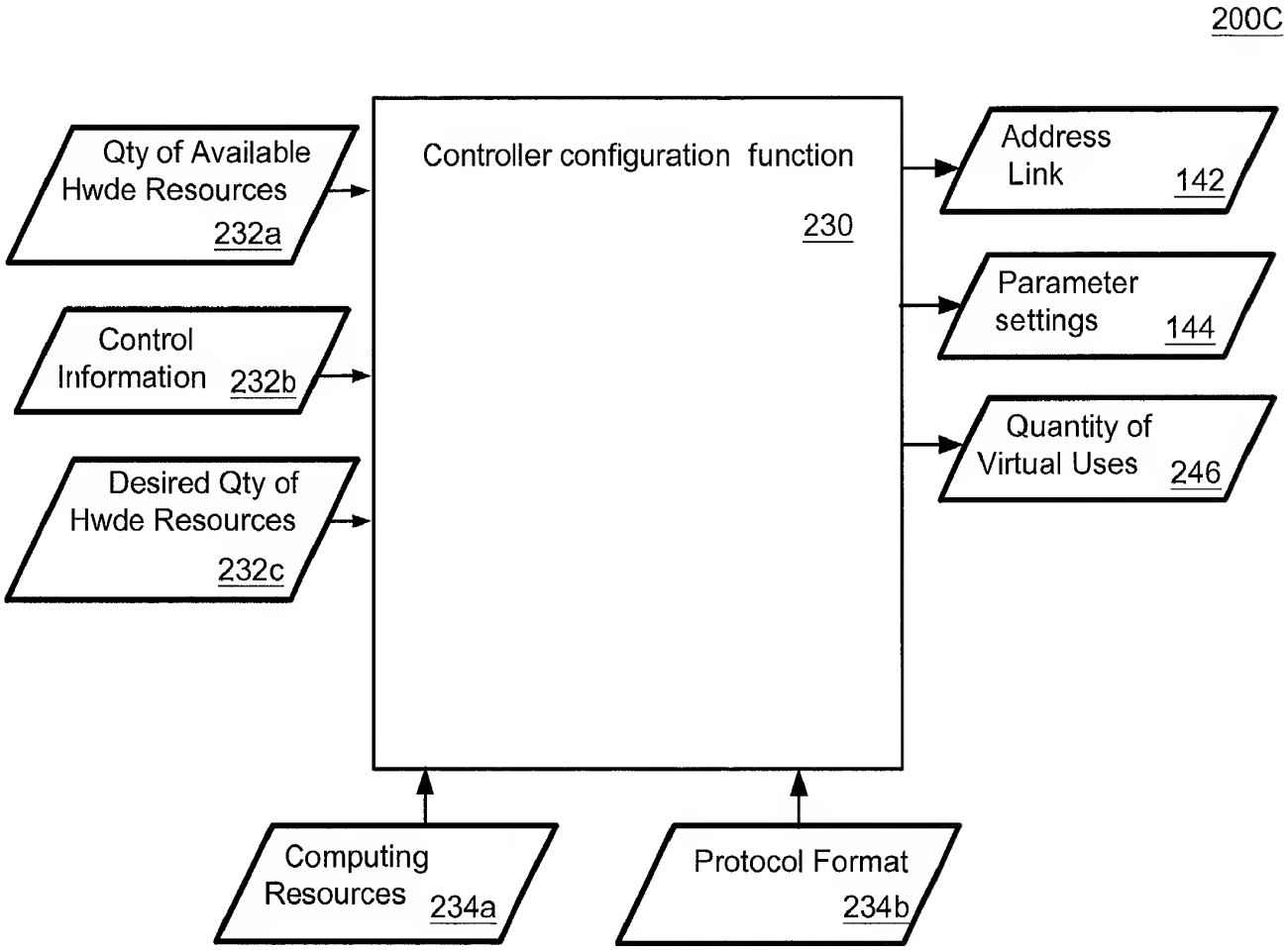

200C
Qty of Available Hwde Resources 232a
Control Information 232b
Desired Qty of Hwde Resources 232c
Controller configuration function 230
Address Link 142
Parameter settings 144
Quantity of Virtual Uses 246
Computing Resources 234a
Protocol Format 234b

FIGURE 2C

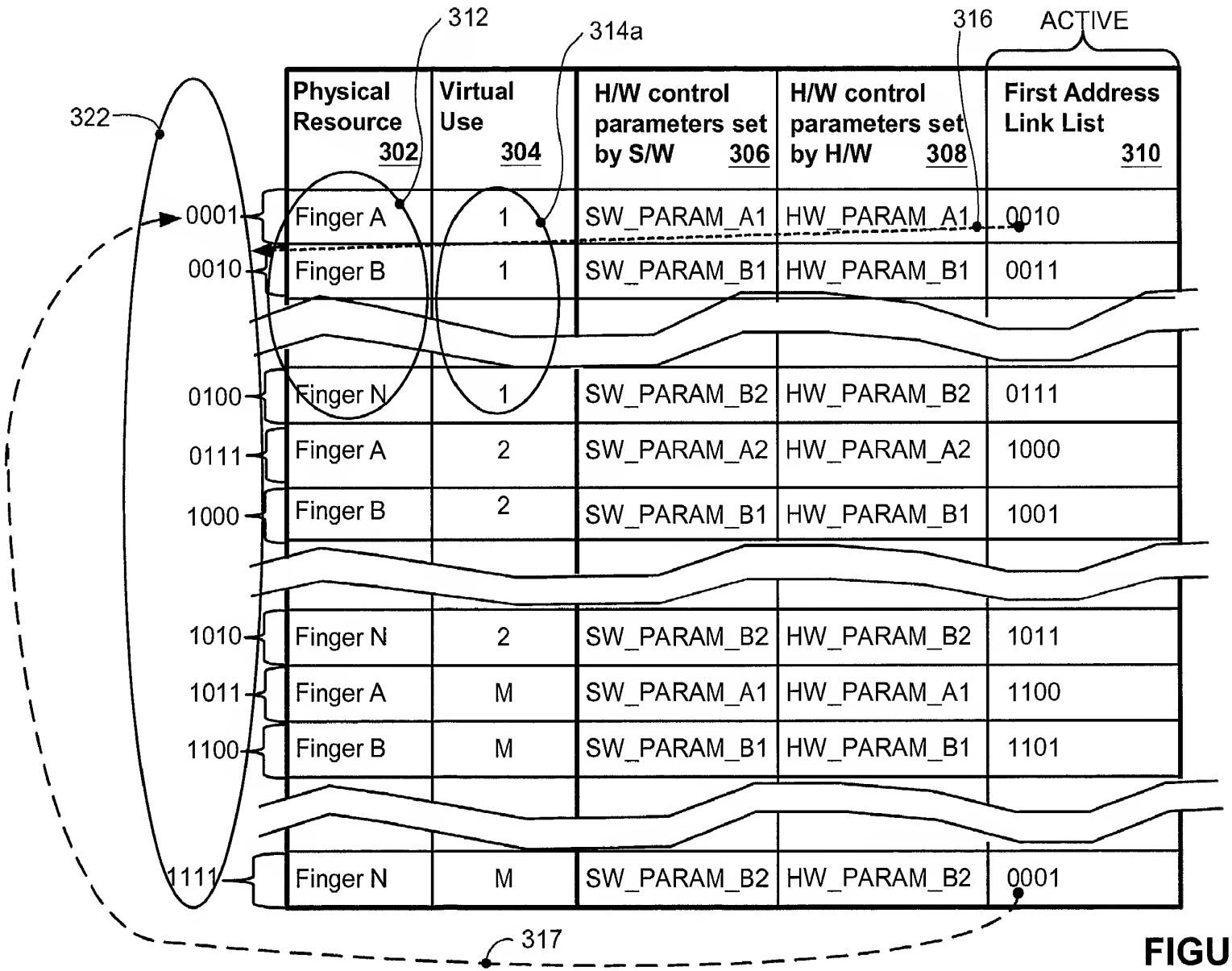

312
314a
316
ACTIVE
300A
322
Physical Resource 302
Virtual Use 304
H/W control parameters set by S/W 306
H/W control parameters set by H/W 308
First Address Link List 310
0001
Finger A
1
SW_PARAM_A1
HW_PARAM_A1
0010
0010
Finger B
1
SW_PARAM_B1
HW_PARAM_B1
0011
0100
Finger N
1
SW_PARAM_B2
HW_PARAM_B2
0111
0111
Finger A
2
SW_PARAM_A2
HW_PARAM_A2
1000
1000
Finger B
2
SW_PARAM_B1
HW_PARAM_B1
1001
1010
Finger N
2
SW_PARAM_B2
HW_PARAM_B2
1011
1011
Finger A
M
SW_PARAM_A1
HW_PARAM_A1
1100
1100
Finger B
M
SW_PARAM_B1
HW_PARAM_B1
1101
1111
Finger N
M
SW_PARAM_B2
HW_PARAM_B2
0001
317

FIGURE 3A

TOSHIBA 3062368

300B

	Physical Resource 302	Virtual Use 304	H/W control parameters set by S/W 306	H/W control parameters set by H/W 308	First Address Link List 310	Second Address Link List 320
0001	Finger A	1	SW_PARAM_A1	HW_PARAM_A1	0010	0010
0010	Finger B	1	SW_PARAM_B1	HW_PARAM_B1	0011	0011
0100	Finger N	1	SW_PARAM_B2	HW_PARAM_B2	0111	0111
0111	Finger A	2	SW_PARAM_A2	HW_PARAM_A2	1000	1000
1000	Finger B	2	SW_PARAM_B1	HW_PARAM_B1	1001	**1010**
1010	Finger N	2	SW_PARAM_B2	HW_PARAM_B2	1011	1011
1011	Finger A	M	SW_PARAM_A1	HW_PARAM_A1	1100	1100
1100	Finger B	M	SW_PARAM_B1	HW_PARAM_B1	1101	**0001**
1111	Finger N	M	SW_PARAM_B2	HW_PARAM_B2	0001	0001

324

326

FIGURE 3B

TOSOTF 3062660

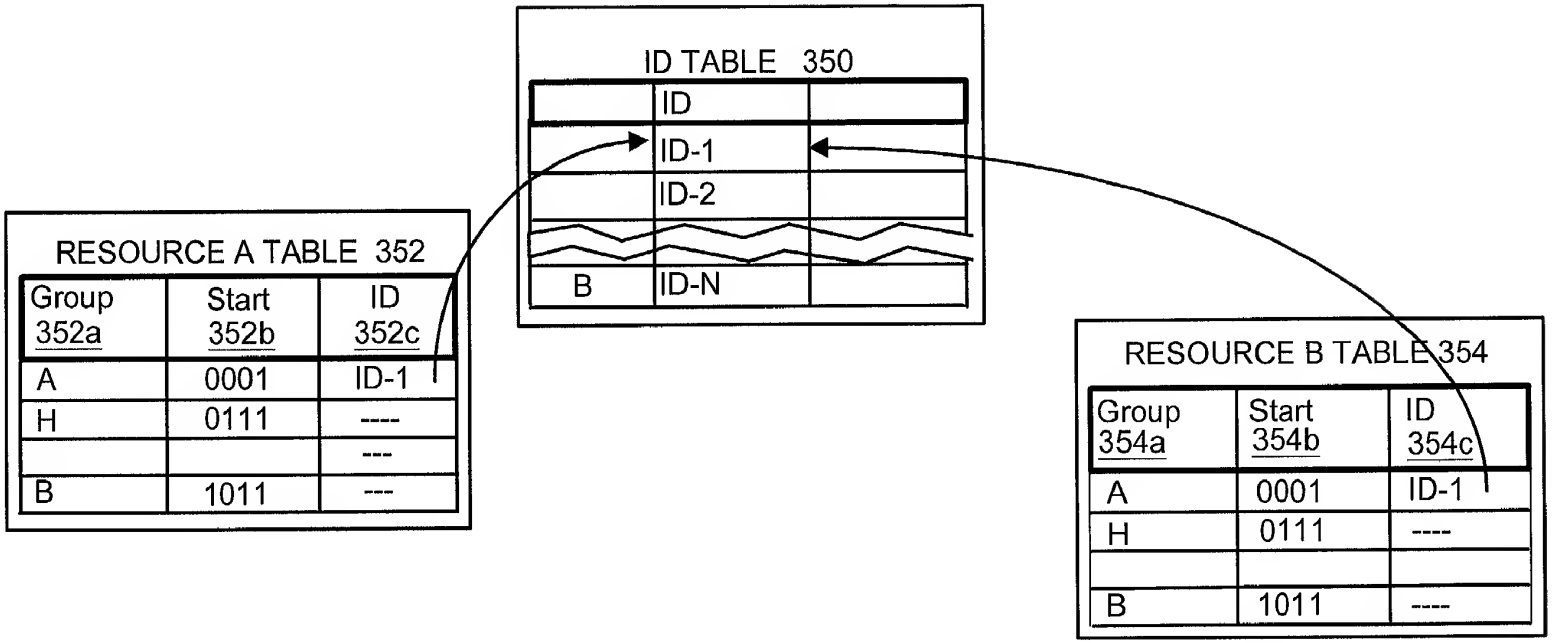
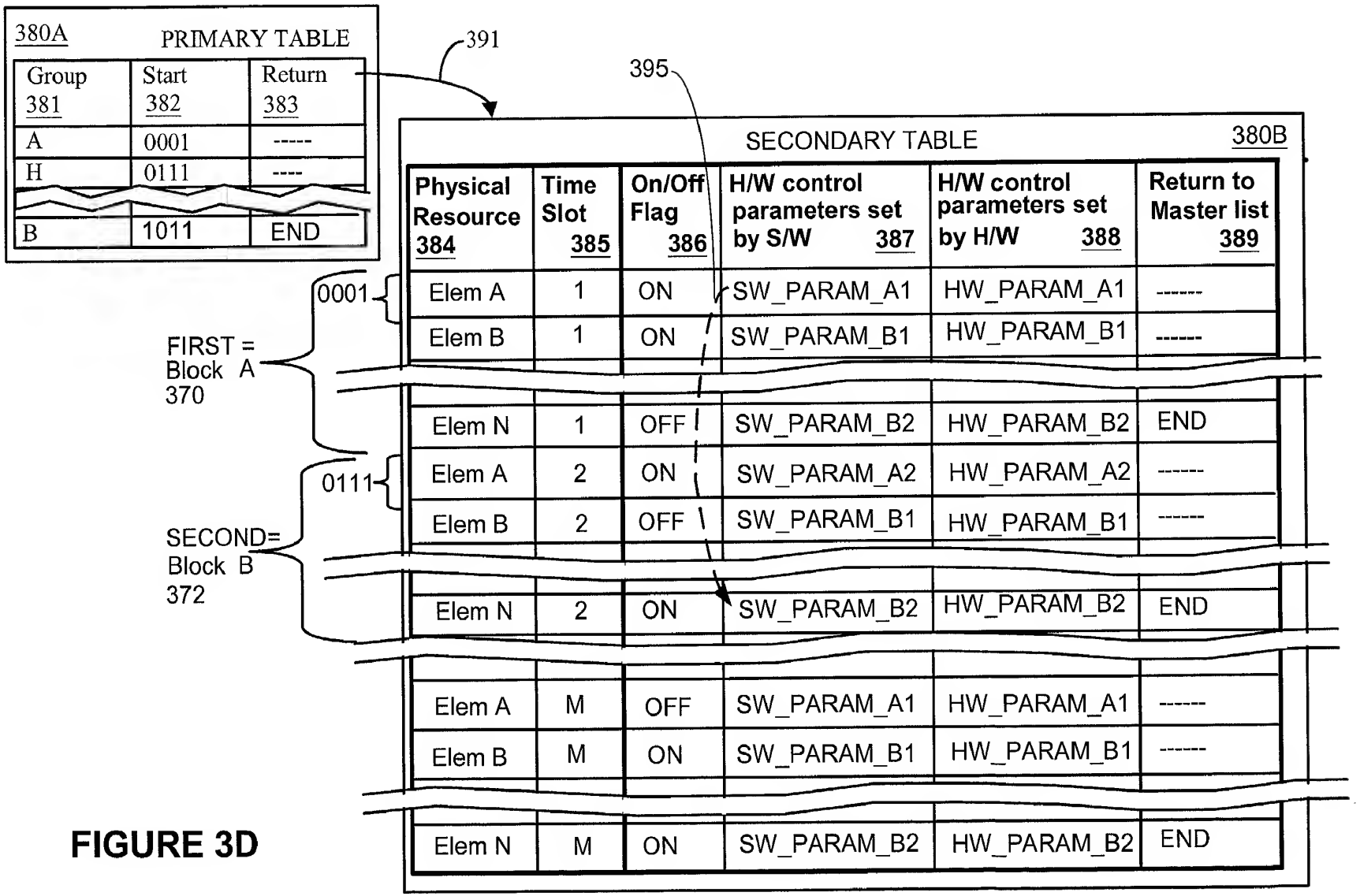


FIGURE 3C


380A
PRIMARY TABLE
Group 381
Start 382
Return 383
A
0001

H
0111

B
1011
END
391
395
SECONDARY TABLE
380B
Physical Resource 384
Time Slot 385
On/Off Flag 386
H/W control parameters set by S/W 387
H/W control parameters set by H/W 388
Return to Master list 389
0001
Elem A
1
ON
SW_PARAM_A1
HW_PARAM_A1

Elem B
1
ON
SW_PARAM_B1
HW_PARAM_B1

FIRST = Block A 370
Elem N
1
OFF
SW_PARAM_B2
HW_PARAM_B2
END
0111
Elem A
2
ON
SW_PARAM_A2
HW_PARAM_A2

Elem B
2
OFF
SW_PARAM_B1
HW_PARAM_B1

SECOND= Block B 372
Elem N
2
ON
SW_PARAM_B2
HW_PARAM_B2
END
Elem A
M
OFF
SW_PARAM_A1
HW_PARAM_A1

Elem B
M
ON
SW_PARAM_B1
HW_PARAM_B1

Elem N
M
ON
SW_PARAM_B2
HW_PARAM_B2
END

FIGURE 3D

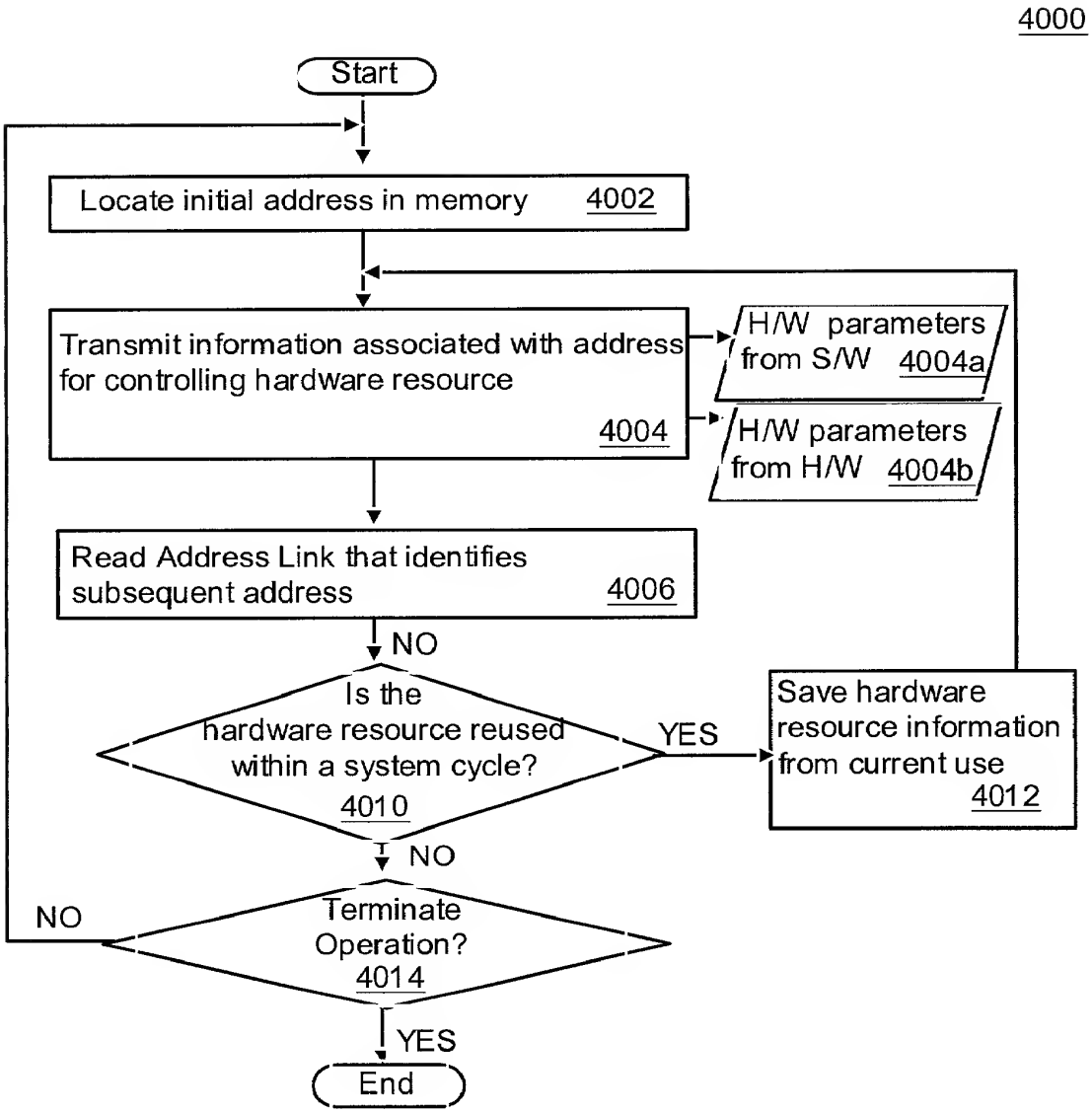

4000
Start
Locate initial address in memory 4002
Transmit information associated with address for controlling hardware resource 4004
H/W parameters from S/W 4004a
H/W parameters from H/W 4004b
Read Address Link that identifies subsequent address 4006
NO
Is the hardware resource reused within a system cycle? 4010
YES
Save hardware resource information from current use 4012
NO
Terminate Operation? 4014
NO
YES
End

FIGURE 4A

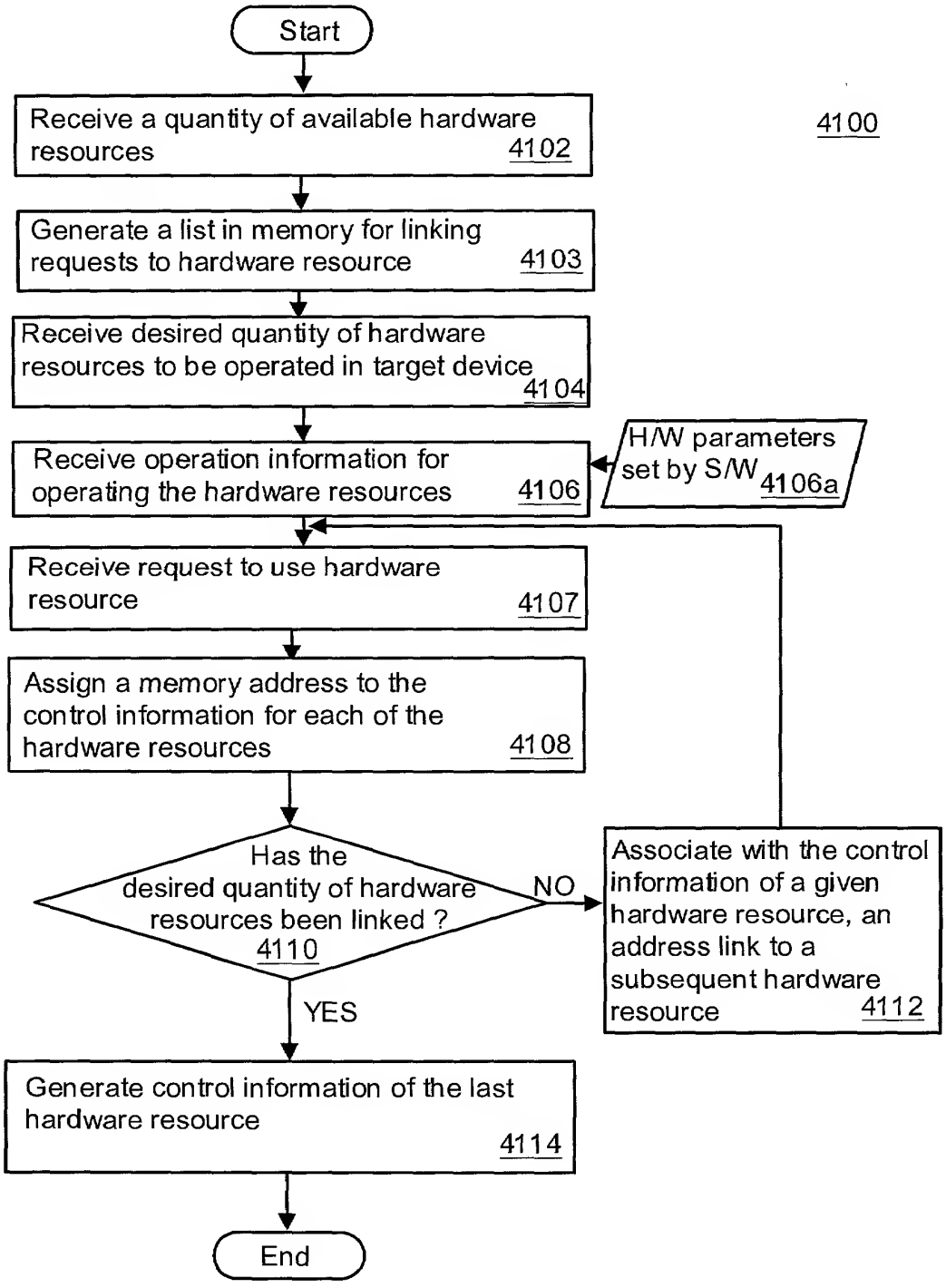

Start
Receive a quantity of available hardware resources 4102
4100
Generate a list in memory for linking requests to hardware resource 4103
Receive desired quantity of hardware resources to be operated in target device 4104
Receive operation information for operating the hardware resources 4106
H/W parameters set by S/W 4106a
Receive request to use hardware resource 4107
Assign a memory address to the control information for each of the hardware resources 4108
Has the desired quantity of hardware resources been linked ? 4110
NO
Associate with the control information of a given hardware resource, an address link to a subsequent hardware resource 4112
YES
Generate control information of the last hardware resource 4114
End

FIGURE 4B

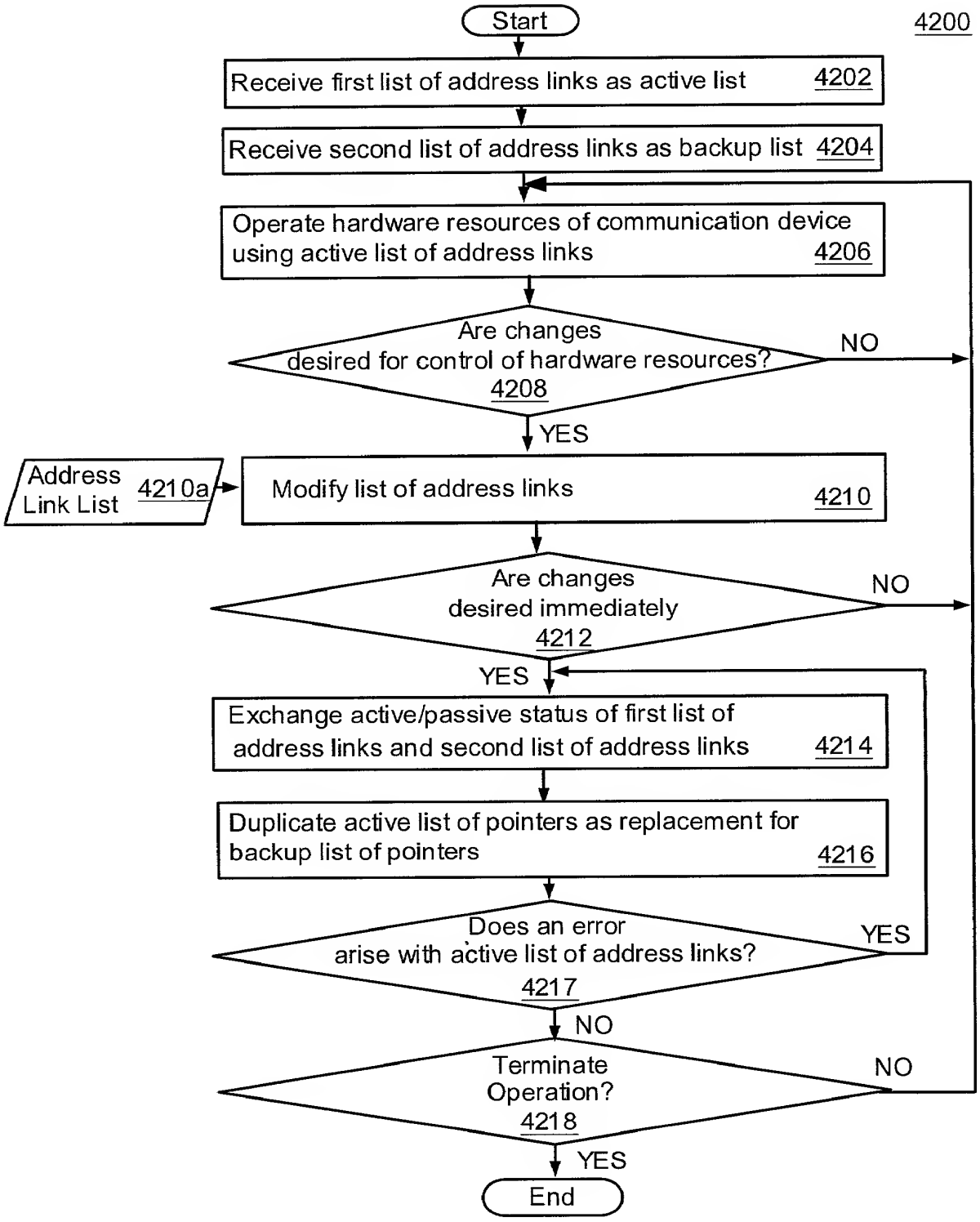

Start
4200
Receive first list of address links as active list 4202
Receive second list of address links as backup list 4204
Operate hardware resources of communication device using active list of address links 4206
Are changes desired for control of hardware resources? 4208
NO
YES
Address Link List 4210a
Modify list of address links 4210
Are changes desired immediately 4212
NO
YES
Exchange active/passive status of first list of address links and second list of address links 4214
Duplicate active list of pointers as replacement for backup list of pointers 4216
Does an error arise with active list of address links? 4217
YES
NO
Terminate Operation? 4218
NO
YES
End

FIGURE 4C